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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10027237	FILING DATE 12/20/2001	CLASS 226	SUBCLASS 2819	GAU 2819	EXAMINER
**APPLICANTS: Zaboronski Oleg; Meulemans Peter;					
**CONTINUING DATA VERIFIED: <i>Nm</i> <i>man</i>					
FOREIGN APPLICATIONS VERIFIED: UNITED KINGDOM 0130255.3 12/18/2001 <i>Man</i>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO 1365.059US1	
TITLE : Logic circuits for performing modular multiplication and exponentiation					

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NOTICE OF ALLOWANCE MAILED 3/17/05		CLAIMS ALLOWED Total Claims 106 Print Claim for 0.6	
ISSUE FEE Amount Due \$1000.00 Date Paid		DRAWING Sheets Drwg. 17 Figs. Drwg. 17 Print Fig. 4	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner <i>[Signature]</i> 4-20-05	
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